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יין פושה				Application Number	10/693,912	
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NT STA	EMENT BY	APPLICA	ANT	First Named Inventor	Nobuo iSHII et al.	
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INFO STATE	'Use as many sheets	as necessary)		Examiner Name	Shih Chao CHEN	
Sheet	1	of	1	Attorney Docket Number	01165-0908-00	

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Examiner	Cite	Document Number	Issue or	Name of Patentee or	Pages, Columns, Lines, Where
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ce.		US-6,713,968 B2	03/30/2004	Ishii et al.	
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Note: Copies of the U.S. Patent Documents are not Required in IDS filed after October 21, 2004

	FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear.	Translation ⁶	
ce		JP 2002-158216	05/31/2002			Yes, abstract	
ce		JP 7-106847	04/21/1995			No	
<u></u>		JP 53-149752	12/27/1978			Yes, abstract	
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Examiner Initials	Cite No.¹	include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶			
ce		English translation of Office Action dated January 4, 2005, issued from the Japanese Patent Office for Japanese Patent Application No. 2002-314623, filed October 29, 2002.				

Examiner Signature	ship-the chen	Date Considered	7/5/05	
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	55.70. 0555466707070			Application Number	10/693,912	7	- 3
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	INFORMATION DISCLOSURE STATEMENT BY APPLICANT			First Named Inventor	Nobuo ISHII et al.	12	- COOJ - H
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ia		JP-A 2000-123997	04/28/2000	Tokyo Electron LTD		Abstract only	
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Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶		
ce		TAKAHASHI et al., "The Suppression of the Reflection by an Inductive Wall of a Power Divider for a Slotted Waveguide Array," The Institute of Electronics Information and Communication Engineers, Technical Report of IEICE, A-P94-7 (1994) pp. 45-50.	abstract only		
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Examiner Signature	Shil-Heo Chon	Date Considered	7/4/05

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